

PATENT NUMBER

257	4024	Subclass
		Class
ISSUE CLASSIFICATION		

U.S. **UTILITY** Patent Application

O.I.P.E.	PATENT DATE
H SCANNED 1.04 G.A. 104	

APPLICATION NO. 09/890120	CONT/PRIOR D F	CLASS 257	SUBCLASS 34	ART UNIT -2811	EXAMINER
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APPLICANTS

Jean-Luc Felloie

TITLE

Mos transistor with dynamic threshold voltage equipped with a current limiting device and method for making same

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL		CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
257	404	257	345	347	376	412	403	404	
INTERNATIONAL CLASSIFICATION		138	149	282	289	217			
H	O	1	L	29	76				

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 8	Figs. Drwg. 17	Print Fig. 5	Total Claims 11	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	[Signature] 1/13 (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	
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